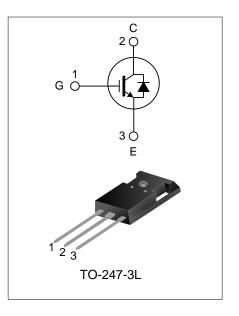
75A, 650V FIELD STOP IGBT

DESCRIPTION

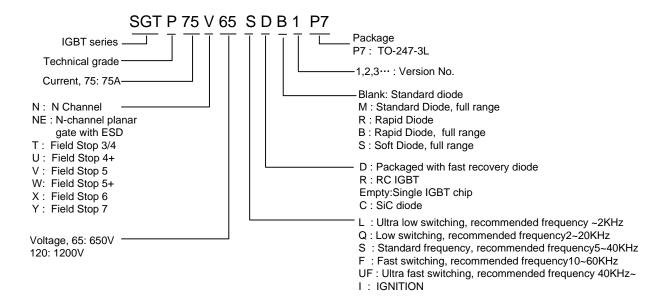
The SGTP75V65SDB1P7 field stop IGBT adopts Silan Field Stop V technology, features low conduction loss and switching loss. This device is applicable to photovoltaic, UPS, SMPS, and PFC fields.

FEATURES

- 75A, 650V, V_{CE(sat)(typ.)}=1.42V@I_C=75A
- Low conduction loss
- Ultra-fast switching
- High input impedance
- T_{Jmax.}=175°C



NOMENCLATURE



ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SGTP75V65SDB1P7	TO-247-3L	P75V65SDB1	Halogen free	Tube

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ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, Tc=25°C)

Chara	cteristics	Symbol	Ratings	Unit
Collector to Emitter Voltage		V_{CE}	650	V
Gate to Emitter Voltage		V_{GE}	±20	V
Transient Gate to Emitter Voltage (t _p ≤10µs, D<0.010)		V_{GE}	±30	٧
Collector Current	T _C =25°C	1	150	۸
Collector Current	T _C =100°C	l _C	75	А
Pulsed Collector C	-		300	Α
Diode Current	T _C =25°C		150	Α
Diode Current	T _C =100°C	l _F	75	Α
Power Dissipation (T _C =25°C)		P _D	395	W
Operating Junction Temperature		TJ	-40∼+175	°C
Storage Temperatu	re Range	T _{stg}	-55∼+150	°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Thermal Resistance, Junction to Case (IGBT)	$R_{ heta JC}$				0.38	°C/W
Thermal Resistance, Junction to Case (FRD)	$R_{ heta JC}$				0.4	°C/W
Thermal Resistance, Junction to Ambient (IGBT)	$R_{\theta JA}$				40	°C/W
Soldering Temperature (in line)	T _{sold}	15 ⁺² ₋₀ sec, 1time			260	°C

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ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED, Tc=25°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Collector to Emitter Breakdown Voltage	BV _{CE}	V _{GE} =0V, I _C =250μA	650			V
C-E Leakage Current	I _{CES}	V _{CE} =650V, V _{GE} =0V			50	μA
G-E Leakage Current	I _{GES}	V _{GE} =20V, V _{CE} =0V			±100	nA
G-E Threshold Voltage	V _{GE(th)}	I _C =250μA, V _{CE} =V _{GE}	3.2	4.0	4.8	V
Collector to Emitter		I _C =75A, V _{GE} =15V, T _C =25°C		1.42	1.85	V
Saturation Voltage	V _{CE(sat)}	I _C =75A, V _{GE} =15V, T _C =125°C		1.62		V
Saturation voltage		I _C =75A, V _{GE} =15V, T _C =150°C		1.67		V
Input Capacitance	C _{ies}	V _{CE} =30V		4823		
Output Capacitance	C _{oes}	V _{GE} =0V		131		pF
Reverse Transfer Capacitance	Cres	f=1MHz		21		
Turn-On Delay Time	T _{d(on)}	V _{CE} =400V - I _C =75A -		35		ns
Rise Time	T _r			39		
Turn-Off Delay Time	T _{d(off)}			194		
Fall Time	T _f	$R_g=10\Omega$		35		
Turn-On Switching Loss	E _{on}	V _{GE} =15V inductive load		1.56		mJ
Turn-Off Switching Loss	E _{off}	T _C =25°C		1.07		
Total Switching Loss	E _{st}	1 _C =25 C		2.63		
Turn-On Delay Time	T _{d(on)}	14 40014		32		
Rise Time	Tr	V _{CE} =400V		24		
Turn-Off Delay Time	$T_{d(off)}$	I _C =37.5A		219		ns
Fall Time	T _f	$R_g=10\Omega$		33		
Turn-On Switching Loss	E _{on}	V _{GE} =15V		0.50		
Turn-Off Switching Loss	E _{off}	inductive load T _C =25°C		0.55		mJ
Total Switching Loss	E _{st}			1.05		
Total Gate Charge	Qg	V _{CE} =520V, I _C =75A, V _{GE} =15V		184		nC
Gate to Emitter Charge	Q _{ge}			33		
Gate to Collector Charge	Q _{gc}			48		

ELECTRICAL CHARACTERISTICS OF FRD (UNLESS OTHERWISE NOTED, Tc=25°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	\/	I _F =75A, T _C =25°C I _F =75A, T _C =150°C		1.55	1.9	V
Diode Forward Voltage	V_{FM}			1.52		V
Diode Reverse Recovery Time	T _{rr}	I _{ES} =75A, dI _{ES} /dt=200A/μs,		123		ns
Diode Reverse Recovery Charge	Qrr	T _C =25°C		0.4		μC

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ELECTRICAL CHARACTERISTICS OF IGBT (UNLESS OTHERWISE NOTED, T_C=150°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	1001/		40		nc
Rise Time	Tr	V _{CE} =400V		86		
Turn-Off Delay Time	$T_{d(off)}$	I _C =75A		272		ns
Fall Time	T _f	$R_g=10\Omega$		80		ı
Turn-On Switching Loss	E _{on}	V _{GE} =15V -		2.6		mJ
Turn-Off Switching Loss	E _{off}	T _C =150°C		2.3		
Total Switching Loss	E _{st}	10=130 C		4.9		
Turn-On Delay Time	T _{d(on)}			37		
Rise Time	Tr	V _{CE} =400V		62		20
Turn-Off Delay Time	$T_{d(off)}$	I _C =37.5A		290		ns
Fall Time	T _f	R_g =10 Ω V_{GE} =15 V inductive load T_C =150° C		40		
Turn-On Switching Loss	Eon			0.78		
Turn-Off Switching Loss	E _{off}			1.08		mJ
Total Switching Loss	E _{st}			1.86		

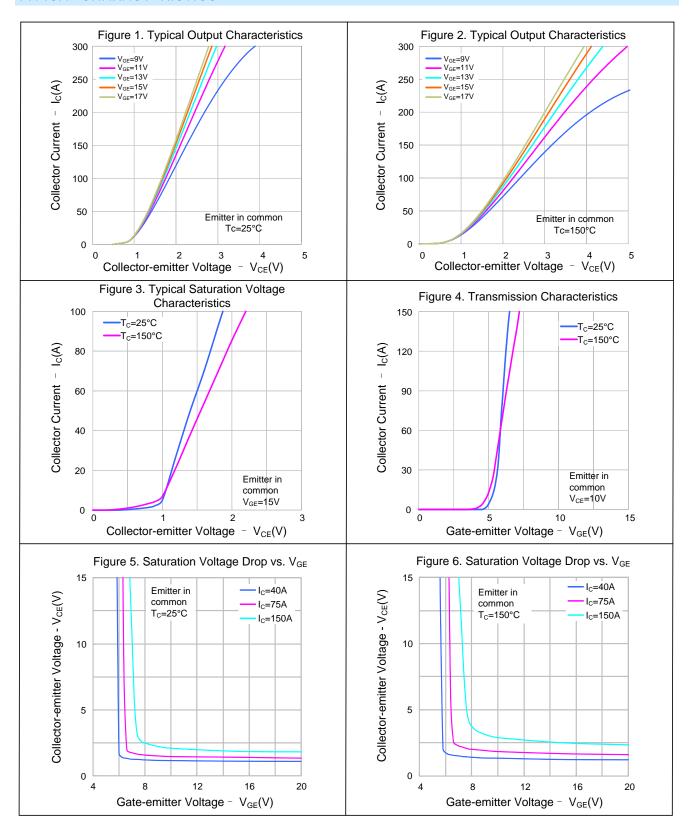
ELECTRICAL CHARACTERISTICS OF FRD (UNLESS OTHERWISE NOTED, T_C=150°C)

Characteristics	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Diode Reverse Recovery Time	T _{rr}	I _{ES} =75A,		218		ns
Diode Reverse Recovery Charge	Q _{rr}	dl _{ES} /dt=200A/µs, T _C =150°C		1.96		μC

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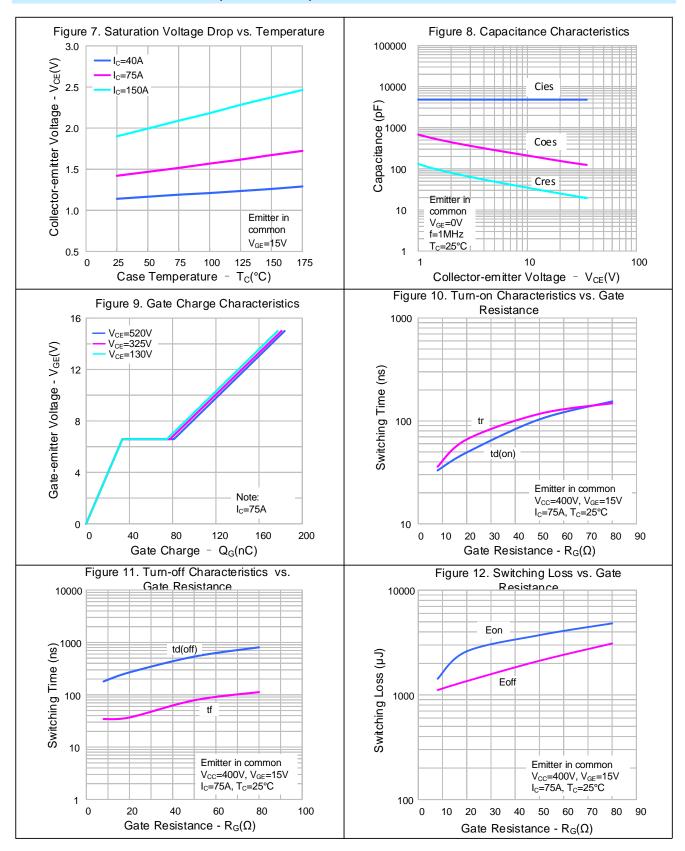
TYPICAL CHARACTERISTICS



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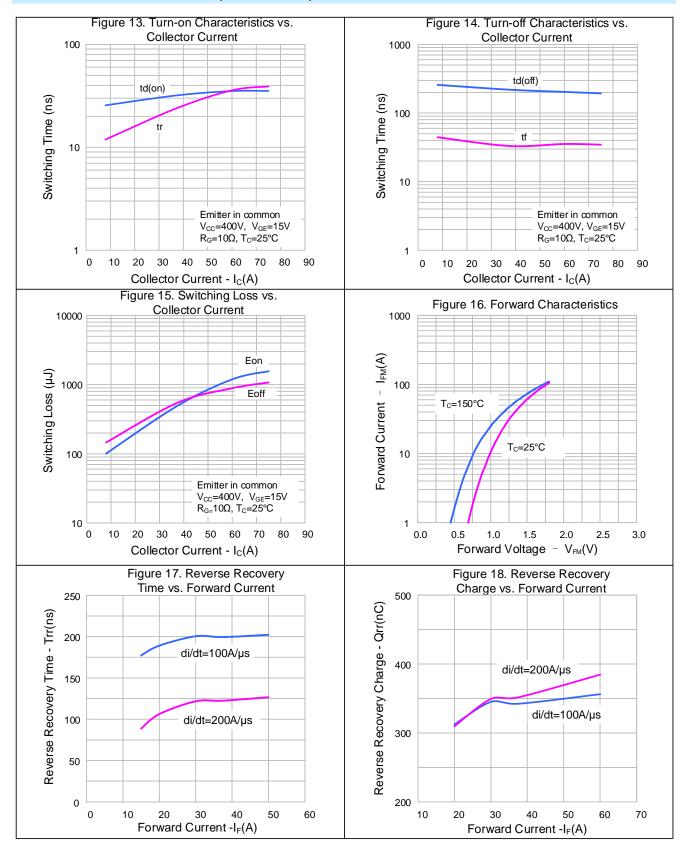
TYPICAL CHARACTERISTICS (CONTINUED)



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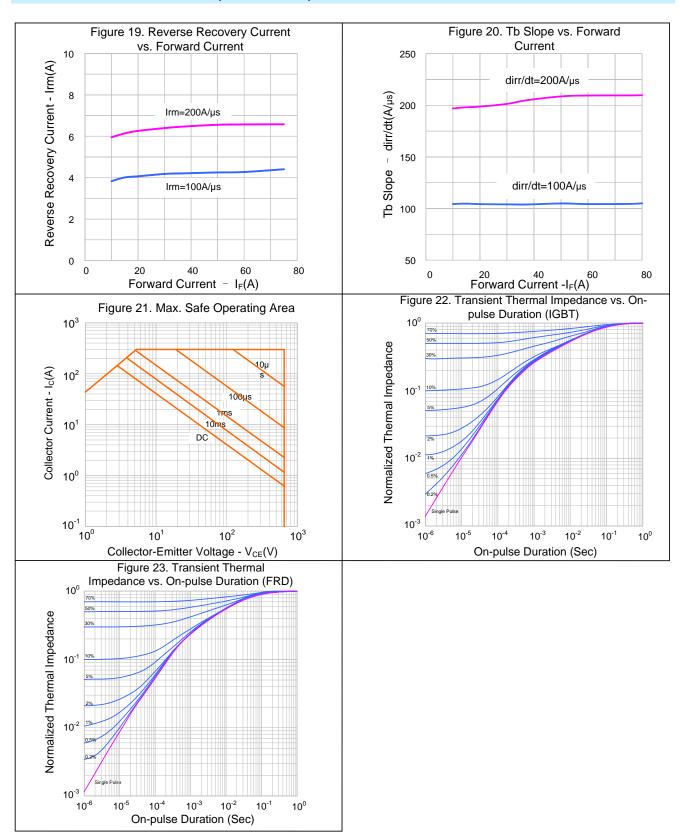
TYPICAL CHARACTERISTICS (CONTINUED)



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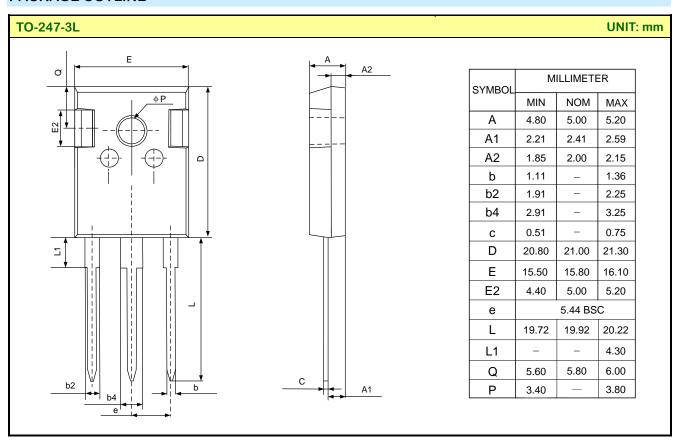
TYPICAL CHARACTERISTICS (CONTINUED)



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PACKAGE OUTLINE





MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

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Important notice:

- 1. Silan reserves the right to make changes of this instruction without notice.
- 2. Customers should obtain the latest relevant information when purchasing and should verify whether such information is latest and complete. Please read this instruction and application manual and related materials carefully before using products, including the circuit operation precautions, etc.
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- 7. Please use and apply product in compliance with all applicable laws and regulations, including but not limited to trade control regulations etc. The product is civil electronic product, please do not use it in non-civil fields.
- 8. Product promotion is endless, our company will wholeheartedly provide customers with better products!
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Rev.: 1.4

Revision History:

Update characteristics

2. Update the important notice

Rev.: 1.3

Revision History:

1. Modify P_D and $R_{\theta JC}$ and update corresponding typical characteristics

2. Update the important notice

Rev.: 1.2

Revision History:

1. Update $V_{CE(sat)}$ when $T_C=25^{\circ}$ C

Rev.:

Revision History:

1. Add V_{GE}

Rev.: 1.0 Revision History:

1. First release

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